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Sub C4

- 6 c. proceeding to execute a subsequent module in said first set of
7 executable modules indicated by said skip value, wherein each
8 module comprises at least one digital signal processing data
9 structure.

1 16. (Amended) An apparatus for executing a first sequence of modules in
2 a first task, said first sequence of modules linked to one another and
3 having at least one sequence of execution, comprising:

- 4 a. means for storing in each of said first sequence of modules a skip
5 value indicating a next module in said sequence of modules to
6 execute;
7 b. means for executing a first module of said first sequence of said
8 modules; and
9 c. means for executing said next module of said sequence of
10 modules indicated by the skip value, wherein each module
11 comprises at least one digital signal processing data structure.

1 17. (Amended) An apparatus for controlling the flow of execution of a
2 first set of executable modules sequentially associated with one another
3 comprising:

- 4 a. means for executing a first module in said first sequence of
5 modules;
6 b. means for determining a skip value associated with said first
7 module; and
8 c. means for proceeding to execute a subsequent module in said
9 first set of executable modules indicated by said skip value,

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wherein each module comprises at least one digital signal

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processing data structure.

Sub
C3

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18. (Twice amended) A method of controlling the execution sequence of a series of modules by a processor, each of said modules associated with one another, comprising the following steps:

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a. executing the first in said series of modules;

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b. determining a skip value N stored in said first in said series of said modules;

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c. if the skip value N stored in said first module is less than zero, then terminating the execution of said series of modules;

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d. else if the skip value N stored in said first module is greater than or equal to zero then proceeding to a N+1th module in said

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series of said modules, wherein each of said modules comprises

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at least one digital signal processing data structure.

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19. (Amended) A method in a computer system of performing a first

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sequence of modules in a first task, said first sequence of modules

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linked to one another and having at least one sequence of execution,

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comprising the following steps:

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a. storing in a first module of said first sequence of modules a skip value N representing a subsequent module in said first sequence of modules to execute, said skip value N comprising either:

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i. an integer less than zero indicating that said first module is a last executable module to be executed in said sequence of modules;

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ii. an integer greater than or equal to zero indicating that said

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process should proceed to said N+1th module subsequent

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to said first module in said first sequence of said modules;

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b. executing the first of said first sequence of said modules; and

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c. executing the subsequent module in said sequence of said

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modules indicated by said skip value, wherein each module of

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said sequence of modules comprises at least one digital signal

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processing data structure.

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